

Single-Channel SCALE Plug-and-Play IGBT Driver

Ultra-compact, high-performance driver for 2-level, 3-level and multilevel converters suitable for the following IGBT modules:

- FZ800R33KF2, FZ800R33KF2C
- FD800R33KF2, FD800R33KF2C, FD800R33KF2C-K

Abstract

The SCALE plug-and-play driver 1SD418F2-FX800R33KF2 is an ultra-compact single-channel intelligent gate driver designed for above mentioned high-voltage IGBTs from Infineon. The driver features a fiber-optic interface, a built-in DC/DC power supply and a selectable operating mode via a jumper.

For drivers adapted to other types of high-power and high-voltage IGBT modules, refer to www.IGBT-Driver.com/go/plug-and-play

Product Highlights Applications ✓ Plug-and-play solution ✓ Three/multi-level converters ✓ Protects the IGBT from short-circuit failure ✓ Two-level converters ✓ Medium-voltage converters ✓ Active clamping of V_{ce} at turn-off ✓ Extremely reliable; long service life ✓ Industrial drives ✓ No electrolytic capacitors Traction ✓ Gate current up to ±18A ✓ Railroad power supplies ✓ Wind-power converters ✓ Electrical insulation 6000 V_{AC} ✓ Radiology and laser technology ✓ Fiber-optic links ✓ Monitoring of supply voltage and fiber optics ✓ Research ✓ Duty cycle 0... 100% ✓ Almost all other conceivable ✓ Built-in DC/DC power supply applications



Important Notes

This data sheet contains only product-specific data. For a detailed description, must-read application notes and common data that apply to the whole series, please refer to the "Description & Application Manual for 1SD418F2 SCALE High-Voltage IGBT Drivers".

When applying SCALE plug-and-play drivers, please note that these drivers are specifically adapted to a particular type of IGBT module. Therefore, the type designation of SCALE plug-and-play drivers also includes the type designation of the corresponding IGBT module. These drivers are not valid for IGBT modules other than those specified. Incorrect use may result in failure.

Mechanical and Electrical Interfaces

Dimensions: 183 x 38 (47) mm. Mounting Principle: Direct screw mount on IGBT.

Interface	Remarks	Part type #
Drive signal input Status output Power supply connector	Fiber-optic receiver (Notes 16,18) Fiber-optic transmitter (Notes 16,19) On-board connector (Note 17)	HFBR-2522 HFBR-1522 77315-101-05
Power supply connector	Designator	Pin numbers

Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Units
Supply voltage V_{DC}	VDC to GND (Note 1)	0	16	V
Gate peak current I _{out}	Note 8	-18	+18	Α
Average supply current Ipc	Note 3		400	mA
Output power DC/DC converter	Notes 3,12		4	W
Switching frequency	Note 12		6	kHz
Test voltage (50Hz/1min)	Primary to output (Note 15)		6000	$V_{AC(eff)}$
DC-link voltage	Note 5		2200	V
Operating temperature	Note 12	-40	+85	°C
Storage temperature		-40	+90	°C



Electrical Characteristics

All data refer to +25°C and V_{DC} = 15V unless otherwise specified

Nominal supply voltage V_{DC} VDC to GND (Note 1) 14.5 15 15.5 V Supply current I_{DC} Without load (Note 2) 120 mA Efficiency η Internal DC/DC converter 85 % Turn-on threshold V_{th} Note 4 13 V Hysteresis on/off Note 4 0.7 V Coupling capacitance C_{io} Primary to output 15 pF
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Short-circuit protection Remarks Min Typ. Max Units
V _{ce} monitoring threshold Between aux. terminals 50 60 V
Response time 3-level mode (Note 11) 8.2 8.8 µs
Response time 2-level mode (Note 6) 9.3 10 µs
Blocking time 2-level mode (Note 7) 1 s
Timing characteristics Remarks Min Typ. Max Units
Turn-on delay $t_{pd(on)}$ Note 13 400 ns
Furn-off delay $t_{pd(off)}$ Note 13 550 ns
Output rise time $t_{r(out)}$ Note 9 100 ns
Output fall time $t_{f(out)}$ Note 9 100 ns
Acknowledge delay time At status output (Note 14) 450 ns
Acknowledge pulse width At status output 0.7 1.8 μs
Gate output Remarks Min Typ. Max Units
Turn-on gate resistor $R_{g(on)}$ Note 8 2.1 Ω
Turn-off gate resistor $R_{q(off)}$ Note 8 3.4 Ω
Aux. gate capacitor C_{ge} 220 nF
Electrical insulation Remarks Min Typ. Max Units
Operating voltage (Note 10) Continuous or repeated 3300 V
Test voltage (50Hz/1min) Primary to output (Note 15) 6000 V _{AC(eff)}
Partial discharge extinction volt. IEC1287 / <10pC 2600 V _{AC(eff)}
Creepage distance Primary to output 20 mm



Footnotes to the key data

- 1) Supply voltages higher than those specified can lead to the destruction of the driver and protection circuits on the output side. The gate-emitter voltage tracks the primary supply voltage. (Not regulated by the supply circuitry or the gate drive unit.)
- 2) Static power consumption of the gate driver.
- 3) If the specified power consumption is exceeded on average, this indicates an overload of the DC/DC converter. The DC/DC converter is not protected against overload.
- 4) Under-voltage monitoring of power supply. For a voltage lower than this limit, the power modules are switched off. The voltage refers to the secondary supply voltage of the gate driver, which is approximately the same as the voltage between VDC and GND.
- 5) This limit is due to active clamping. Refer to the "Description and Application Manual for 1SD418F2 SCALE High-Voltage IGBT Drivers".
- 6) Pulse width of the direct output of the gate drive unit. (Excluding the delay of the gate resistors.)
- 7) Duration of blocking the command input (keeping the gate driver and the IGBT in the off-state) after fault detection, i.e. power supply Under-voltage lock out, or only in 2-level mode short-circuit detection. (For three/multilevel mode, turn-off under the short-circuit condition is managed by the host controller.)
- 8) The gate current is limited by on-board gate resistors.
- 9) Refers to the direct output of the gate drive unit. (Excluding the delay of the gate resistors.)
- 10) Maximum continuous or repeatedly applied DC voltage or peak value of the repeatedly applied AC voltage between the power supply inputs and all other terminals.
- 11) Including the delay of external fiber optic links. Measured from turn-on transition at direct output of the gate drive unit (not covering the delay of the gate resistors) to the transition of status signal at the host controller side optical receiver.
- 12) Application-specific self-heating of gate drivers and IGBT modules, especially at high switching frequency, must be taken into account. The switching frequency is commonly limited due to switching losses of the IGBT modules. Because CONCEPT cannot predict how the drivers will be incorporated in the user's application, no binding recommended value for self-heating and thus for the maximum useable output power can be made. It is therefore recommended to check the gate driver's ambient temperature within the system.
- 13) Including the delay of external fiber-optic links. Measured from the transition of turn-on or turn-off command at the optical transmitter on the host controller side to direct output of the gate drive unit. (Excluding the delay of the gate resistors.)
- 14) Including the delay of external fiber-optic links. Measured from the transition of turn-on or turn-off command at the optical transmitter on the host controller side to the transition of the acknowledge signal at the optical receiver on the host controller side.
- 15) The test voltage may be applied only once during one minute. It should be noted that with this (strictly speaking obsolete) test method, some (minor) damage occurs to the insulation layers due to the partial discharge. Consequently, this test is not performed at CONCEPT as a series test. In the case of repeated insulation tests (e.g. module test, equipment test, system test) the subsequent tests should be performed with a lower test voltage: the test voltage is reduced by 10% for each additional test. The more modern if more elaborate partial-discharge measurement is better suited than such test methods as it is almost entirely non-destructive.
- 16) The transceivers required at the host controller side are not delivered with the gate driver. It is recommended to use the same types as used in the gate driver. For product information refer to www.IGBT-Driver.com/go/fiberoptics
- 17) The customer-side connector is not supplied with the gate driver, but via FCI Inc. Recommended crimp contact housing: order code 65039-032; recommended crimp contacts: 5 pcs, order code 48236-002. Refer to www.IGBT-Driver.com/go/fci
- 18) The recommended transmitter current at the host controller is 30-35mA, suitable for plastic optic fibers with a length of less than 2.5 meters. Higher current may increase jitter or delay at turn-off.
- 19) The transmitter current at the gate driver is 30-35mA.



Important Notice

The data contained in this product data sheet is intended exclusively for technically trained staff. Handling all high-voltage equipment involves risk to life. Strict compliance with the respective safety regulations is mandatory!

Any handling of electronic devices is subject to the general specifications for protecting electrostatic-sensitive devices according to international standard IEC 747-1, Chapter IX or European standard EN 100015 (i.e. the workplace, tools, etc. must comply with these standards). Otherwise, this product may be damaged.

Legal Disclaimer

This data sheet specifies devices but cannot promise to deliver any specific characteristics. No warranty or guarantee is given – either expressly or implicitly – regarding delivery, performance or suitability.

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Ordering Information

The general terms and conditions of delivery of CT-Concept Technologie AG apply.

Related IGBT

CONCEPT Driver Type #

Infineon (eupec) FD800R33KF2 Infineon (eupec) FD800R33KF2C Infineon (eupec) FD800R33KF2C(-K) Infineon (eupec) FZ800R33KF2	1SD418F2-FX800R33KF2 1SD418F2-FX800R33KF2 1SD418F2-FX800R33KF2 1SD418F2-FX800R33KF2
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Infineon (eupec) FZ800R33KF2C	1SD418F2-FX800R33KF2

Information about Other Products

For drivers adapted to other high-voltage or high-power IGBT modules

Direct link: www.IGBT-Driver.com/go/plug-and-play

For other drivers and evaluation systems

Please click: www.IGBT-Driver.com

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